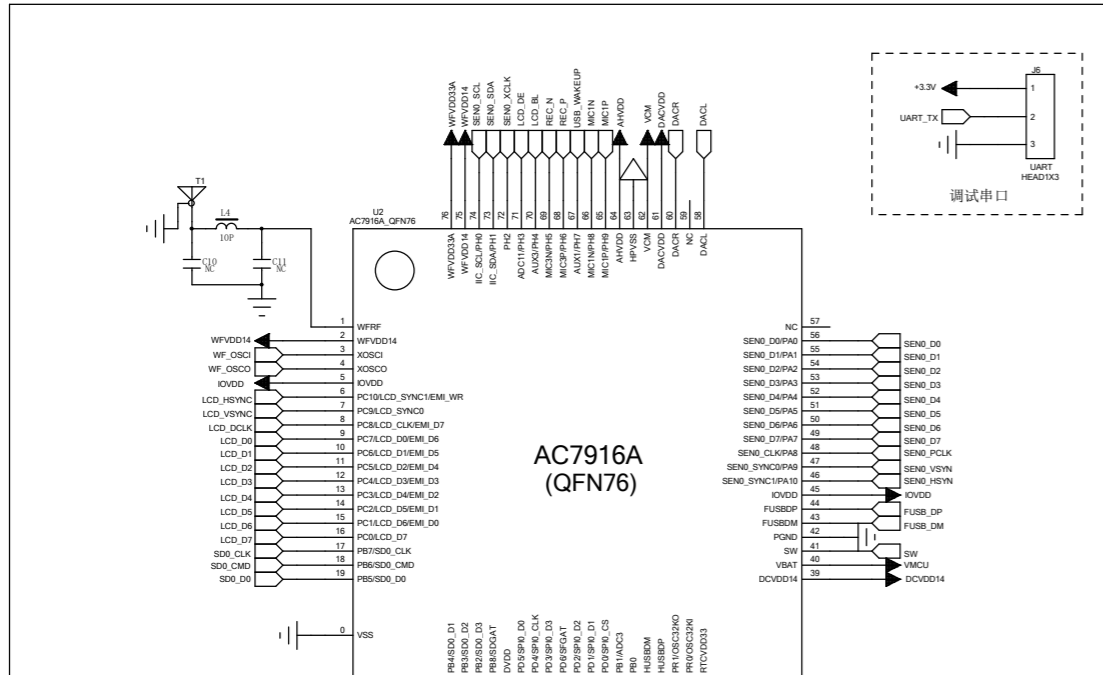


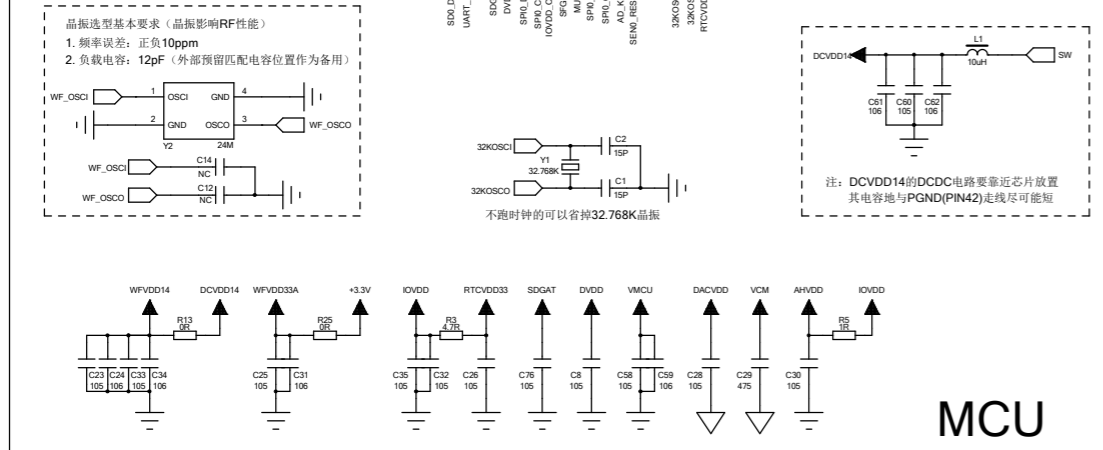
D

D



C

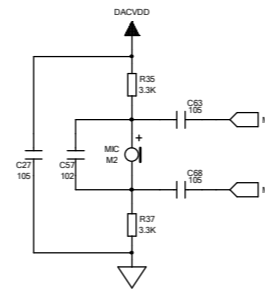
C



B

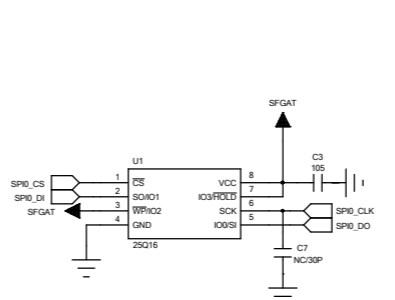
B

注: 根据具体需求选择单/双麦进行设计
而要求较高情况下可调整IO采用数字硅麦



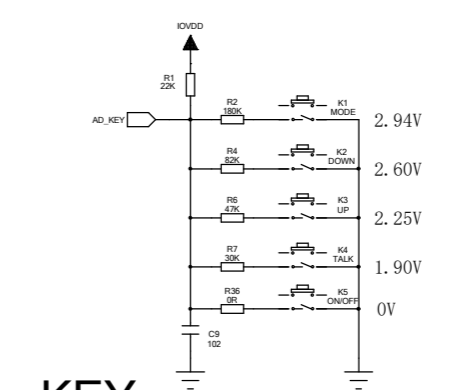
MIC

关于电源控制: (关机低功耗)
SFGAT用作FLASH电源及外部3.3V DCDC的使能脚



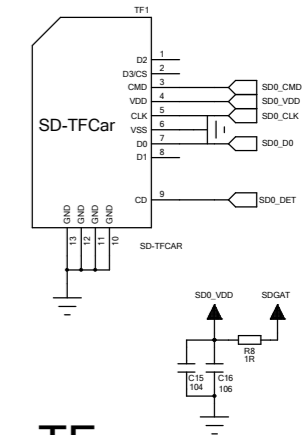
FLASH

注: 软关机后是通过下降沿唤醒主控的, 开关机键值最好定为0V.
若按键较多时, 为防止临近按键误判到唤醒, 开关机按键可定义为长按操作或独立使用有WKUP功能的IO



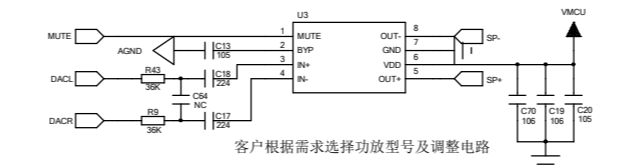
KEY

TF卡使用1线模式



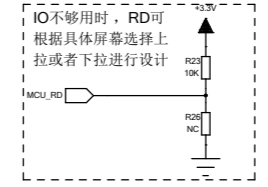
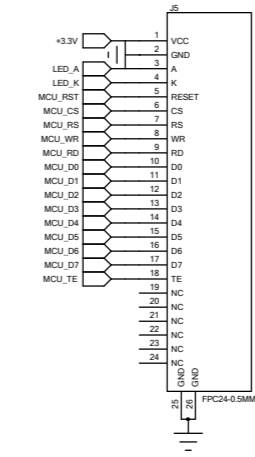
TF

备注1: 参考原理图默认采用RGB屏, RGB屏与MCU屏两者二选一
备注2: MCU屏的WR/RD及Data信号有固定IO (具体可参考芯片规格书), 其他信号可使用GPIO控制

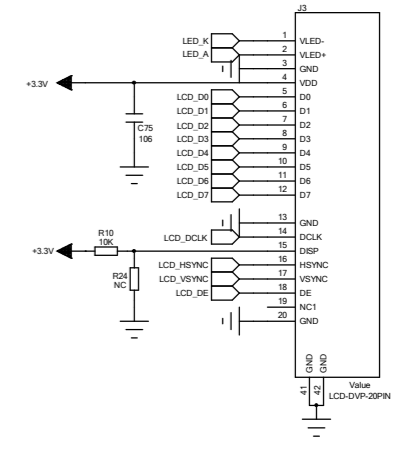


PA

8位MCU屏



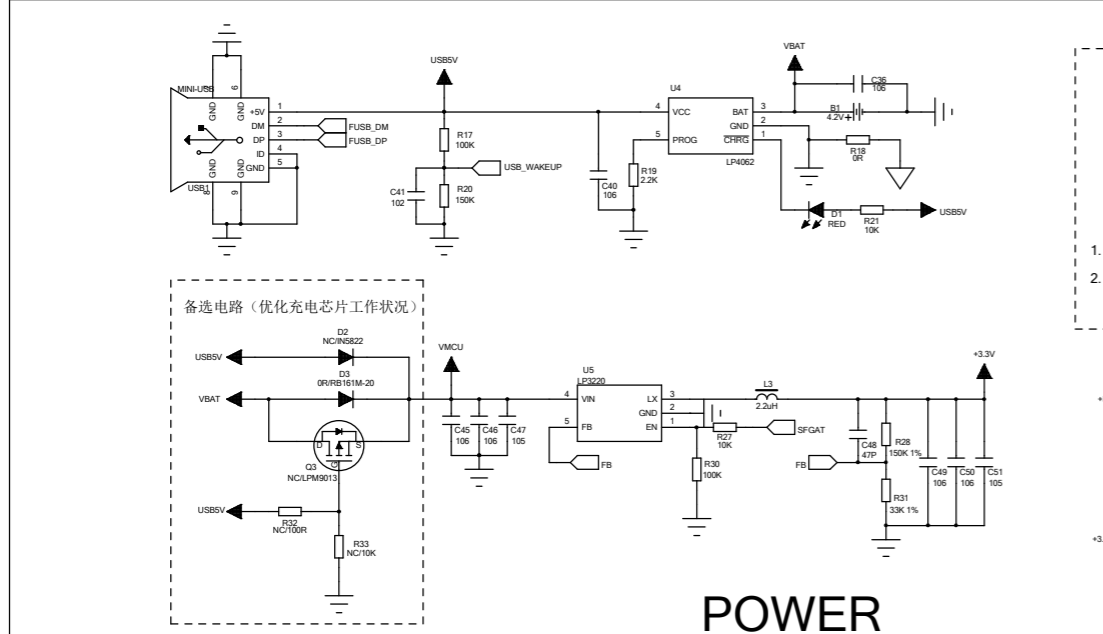
8位RGB屏



LCD

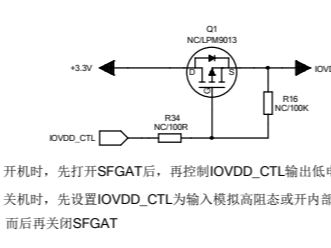
A

A

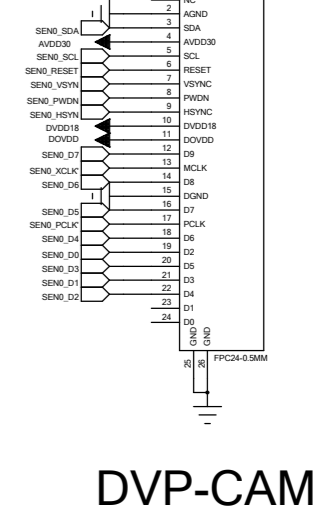
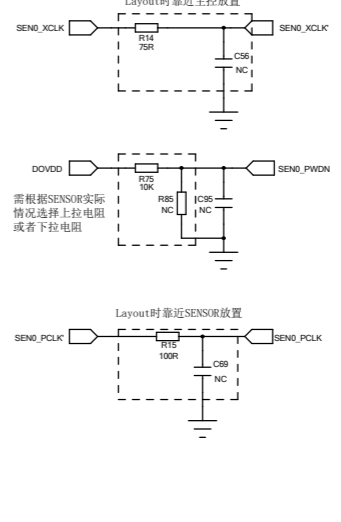
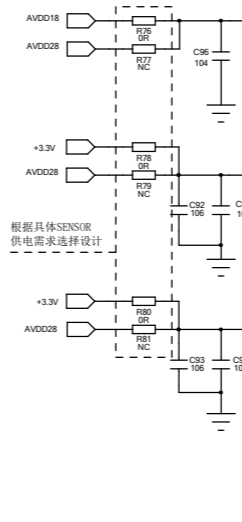
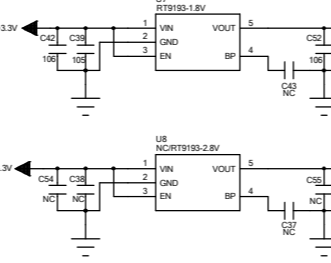


POWER

备注: 以下电路默认NC, 客户根据需求选用



- 开机时, 先打开SFGAT后, 再控制IOVDD_CTL输出低电平;
- 关机时, 先设置IOVDD_CTL为输入模拟高阻态或开内部上拉电阻而后关闭SFGAT



DVP-CAM